



AT- SFV

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
NOTICE OF APPEAL FROM THE PRIMARY EXAMINER
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

In re application of:

KIAN TENG ENG ET AL.

Serial No.: 09/766,477 (TI-22944.2)

Filed: January 19, 2001

For: VERTICAL BALL GRID ARRAY INTEGRATED CIRCUIT PACKAGE

Group Art Unit: 2827

Examiner James M. Mitchell

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

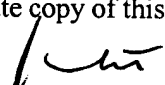
Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated April 21, 2006 of the Primary Examiner finally rejecting claims 21-25, 27, 28 and 37, all of the rejected claims.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection
☐ was granted on
☐ is requested for _____ month(s).
2. ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☐ Fee \$500.00:
☐ Not required (*Fee paid in prior appeal*)
☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Account No. 20-0668. A duplicate copy of this sheet is enclosed.

Signature [Rule 191(b)]


Jay M. Cantor, Reg. No. 19906
Texas Instruments Incorporated
P. O. Box 655474, M/S 3999
Dallas, TX 75265
(301) 424-0355
(972) 917-5293

Post Office Address
(to which correspondence is to be sent)